

ABSTRACT OF THE DISCLOSURE

A system of modeling an integrated circuit is described in which a circuit component model 6 is operated upon by a delay calculator 16 using a subset of associated timing and rule data 20. The delay calculator 16 calculates signal transition delays within the circuit component model 6. The output of the delay calculator 16 is searched to identify corresponding signal transitions within the original model 4. Matching transitions are then updated with the calculated delay information augmented with the full set of associated timing and rule data 23 (with constraint data).

[Figure 2]